1. A cellular telephone comprising:

a first processor;

a second processor;

a first bus coupling said first and second processors; and

a device to selectively bypass the first processor.

2. The telephone of claim 1 wherein said first processor is an applications processor.

3. The telephone of claim 1 including a keypad, said first processor coupled to said keypad to receive keypad inputs.

4. The telephone of claim 1 including a display, said first processor coupled to said display to provide outputs to said display.

5. The telephone of claim 2 wherein said second processor is a baseband processor.

6. The telephone of claim 1 wherein said device selectively bypasses the first processor if the first processor fails to respond.

7. The telephone of claim 1 wherein the second processor selectively bypasses the first processor to make an emergency call.

8. The telephone of claim 1 wherein said telephone includes a keypad, keypad entries being provided to said first processor, said device selectively shunting said keypad entries to said second processor.

9. The telephone of claim 1 including a display, said display coupled to receive outputs from said first processor, said device to selectively bypass the first processor to provide outputs to said display from said second processor.

10. The telephone of claim 1 including a display and a keypad, said first processor coupled to said display and said keypad and said second processor coupled to said display and said keypad through said first processor and said device.

11. A method comprising:

establishing communications between an input/output device and a first processor; and

in response to the detection of an event, providing said communications to a second processor.

12. The method of claim 11 including selectively coupling keypad entries to a second processor when a first processor fails to respond.

13. The method of claim 11 including coupling keypad entries directly to the first processor except when the first processor fails to respond.

14. The method of claim 11 including detecting an emergency call and in response to the detection of an emergency call, coupling keypad entries directly to a baseband processor.

15. The method of claim 11 wherein detecting an event includes detecting the failure of a first processor to respond.

16. The method of claim 15 including detecting the failure of the first processor to respond within a predetermined amount of time.

17. The method of claim 11 including coupling said second processor to said first processor and coupling said first processor directly to a keypad and a display.

18. The method of claim 17 including selectively coupling said display and said keypad directly to said second processor.

19. The method of claim 11 including providing a first processor which acts as an applications processor.

20. The method of claim 19 including providing a second processor that acts as a baseband processor.

21. An article comprising a medium storing instructions that enable a processor-based system to:

establish communications between an input/output device and a first processor; and

in response to the detection of an event, provide said communications to a second processor.

22. The article of claim 21 further storing instructions that enable the processor-based system to selectively couple keypad entries to a second processor when a first processor fails to respond.

23. The article of claim 21 further storing instructions that enable the processor-based system to couple keypad entries directly to the first processor except when the first processor fails to respond.

24. The article of claim 21 further storing instructions that enable the processor-based system to detect an emergency call and in response to the detection of an emergency call, couple the keypad entries directly to a baseband processor.

25. The article of claim 12 further storing instructions that enable the processor-based system to detect the failure of the first processor to respond.

26. The article of claim 25 further storing instructions that enable the processor-based system to detect the failure of the first processor to respond within a predetermined amount of time.

27. The article of claim 21 further storing instructions that enable the processor-based system to couple said second processor to said first processor and couple said first processor directly to a keypad and a display.

28. The article of claim 27 further storing instructions that enable the processor-based system to selectively couple said display and said keypad directly to said second processor.

29. The article of claim 21 further storing instructions that enable the processor-based system to establish communications between an input/output device and a first processor that is an applications processor.

30. The article of claim 29 further storing instructions that enable the processor-based system to provide a second processor that acts as a baseband processor.